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OPTICAL DISC DEVICE

CROSS-REFERENCE TO RELATED APPLICATION

This application claims the priority benefit of Japanese application serial no. 2000-259569, filed August 29, 2000.

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BACKGROUND OF THE INVENTION

1. Field of the Invention:

[0001] The present invention relates in general to an optical disc device. More specifically, the present invention relates to an optical disc device capable of recording data on an erasable optical disc.

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2. Description of Related Art:

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[0002] There are several types of recordable optical discs, such as a write-once optical disc and an erasable optical disc. The write-once optical disc, such as a compact disc - recordable (CD-R) and the erasable optical disc such as a compact disc-rewritable (CD-RW) or a digital versatile disc-rewritable (DVD-RW) have pregrooves for guiding on the recording optical discs. The pregrooves are wobbled slightly along the radial direction of the optical disc with a central frequency of 22.05KHz. The address data in recording, termed as absolute time in pregroove (ATIP), are recorded in a multiplex mode by FSK modulation with a maximum deviation frequency of ± 1 KHz.

[0003] In regards to the tracking and focusing servo circuits of the optical disc device

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for recording and reproducing data on/from the optical disc, light beams are illuminated on the optical disc and the light beams reflected by the optical disc are detected by a number of photo detectors. According to a preset algorithm, the detected signals are used for generating the servo signals, which are used for driving an actuator to perform the tracking and focusing servo controls.

[0004] In regards to the optical disc device for recording and reproducing data on/from the erasable optical disc, such as the CD-RW, a 4T recording signal (EFM signal) is recorded as shown in Fig. 8A. Referring to Fig. 8B, a recording signal value of "0" of the power of the light beam corresponds to the erase power P_e , and a recording signal value of "1" corresponds to the write power P_w and the read power P_b , wherein P_w and P_b alternate. In addition, the base time interval T is a period in a frequency of 4.32MHz and of about 230 nsec.

[0005] Therefore, when data is reproduced, the incident light beams, which have the read power, are reflected by the optical disc and the reflected light beams are detected by the photo detectors and according to the detected signal, a tracking error signal is generated. When the data is recorded, the incident light beams, which have the write power and the erase power, are reflected by the optical disc and the reflected light beams are detected by the photo detectors and the sampling/hold circuits sample and hold the detected signal when the incident light beams have the erase and according to the sampling/hold signal, a tracking error signal is generated.

[0006] Figs. 9 and 10 are exemplary circuit diagrams of a conventional servo signal generator. Referring to Fig. 9, spots of the light beams are illuminated on the optical

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disc and the light beams reflected by the optical disc are detected by four divided photo detectors 10A, 10B, 10C and 10D. The respective detected signals by the photo detectors 10A, 10B, 10C and 10D are transmitted to the corresponding sampling/hold circuits SA, SB, SC and SD. Based on the sampling/hold signal from the terminal 12, for example, a high-level timing, each of the sampling/hold circuits SA, SB, SC and SD samples and holds the detected signals, which are then respectively provided to the corresponding amplifiers GA, GB, GC and GD. According to a gain-switching signal from a terminal 14, the gains of the amplifiers GA, GB, GC and GD switch to different gains, which correspond to the foregoing detected signals when data are recorded and reproduced. Switching to different gains also causes the levels of the detected signals to have the same level when data are recorded and reproduced, which are then respectively outputted from the terminals 16A, 16B, 16C and 16D.

[0007] According to the foregoing servo signal generator, the variations in the offset voltage generated by the gain-switching of the amplifiers GA, GB, GC, GD are inputted to the servo signal calculating device. The total offset voltage of the generated servo signals is therefore affected. Particularly, in the situation of the addition signal, unlike the situation of the subtraction signal that the offset voltages can be eliminated, the influence of the offset voltages to the addition signal cannot be ignored. Therefore, the offset voltages have to be changed whenever the gain of the amplifier is changed.

[0008] Referring to Fig. 10, the detected signals A, B, C, D outputted from the amplifiers GA, GB, GC, GD are inputted to the calculating devices 18, 20. The calculating device 18 adds the detected signals A, B, C, D with a correction offset voltage Vosadd of the addition signal from the terminal 19 to generate an addition

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signal (A+B+C+D). The addition signal, which serves as the reflected light monitoring signal, is then outputted from the terminal 22. In addition, the calculating device 20 adds and subtracts the detected signals A, B, C, D with a correction offset voltage Vossb of the subtraction signal from the terminal 21 to generate a subtraction
 5 signal (A+B)-(C+D), which serves as the servo signal, and is then outputted from a terminal 24.

[0009] Assuming the offset voltages of the detected signals A, B, C and D respectively outputted from the sampling/hold circuits SA, SB, SC, SD are Va, Vb, Vc, and Vd, and the input offset voltages of the amplifiers GA, GB, GC, GD are Vga, Vgb, Vgc, and Vgd respectively, the gains of the amplifiers GA, GB, GC, GD are G, and the
 10 calculating devices 18, 20 have no offset voltages. Under this condition, the total offset voltage of the addition signal Vadd is represented by the equation (1) below.

$$[0010] \quad V_{add} = G(V_a + V_b + V_c + V_d + V_{ga} + V_{gb} + V_{gc} + V_{gd})$$

(1)

Namely, if the correction offset voltage Vosadd of the addition signal is -Vadd, the total
 15 offset voltage of the addition signal can be compensated.

$$V_{osadd} = -G(V_a + V_b + V_c + V_d + V_{ga} + V_{gb} + V_{gc} + V_{gd})$$

However, due to the presence of the gain G of the amplifier in the above equation, there is a problem that the correction offset voltage Vosadd of the addition signal has to be changed with the gain G of the amplifier.

20 [0011] In addition, the total offset voltage of the subtraction signal Vsub is

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represented by the equation (2) below:

$$V_{sub} = G [(V_a + V_b + V_{ga} + V_{gb}) - (V_c + V_d + V_{gc} + V_{gd})] \quad (2)$$

Namely, if the correction offset voltage V_{osub} of the subtraction signal is $-V_{sub}$, the total offset voltage of the subtraction signal can be compensated.

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$$V_{osadd} = -G [(V_a + V_b + V_{ga} + V_{gb}) - (V_c + V_d + V_{gc} + V_{gd})]$$

However, due to the presence of the gain G of the amplifier in the above equation, there is a problem that the correction offset voltage V_{osub} of the subtraction signal has to be changed with the gain G of the amplifier.

SUMMARY OF THE INVENTION

10 [0012] According to the foregoing description, an object of this invention is to provide an optical disc device, which does not need to change the offset voltage regardless of a change in the gain of the amplifier.

[0013] According to the object mentioned above, the invention provides an optical disc device for changing intensities of light beams illuminated on an optical disc. The optical disc device comprises the following elements. A photo detecting device
15 divided into a plurality of photo detectors are used for detecting reflected light beams of the light beams illuminated on an optical disc. A plurality of amplifiers are used for changing gains to respectively amplify output signals of the photo detectors when data are recorded and reproduced on/from the optical disc. A calculating device is also
20 used for calculating the output signals of the amplifiers to generate the servo signals.

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By adding correction offset signals for correcting offset voltages of the amplifiers and the photo detectors to the amplifiers, the correction offset signals are amplified and therefore it is not necessary to change the correction offset voltages even though the gain of the amplifier is changed.

5 [0014] The calculating device further comprises a first calculating device and a second calculating device for respectively performing different operations on the output signals of the amplifiers. Because the correction offset signals respectively added to the amplifiers further comprise a first correction offset value that eliminates the offset voltages from a result of the first calculating device, and a second correction offset value that eliminates the offset voltages calculated from the second calculating device, the offset voltages calculated from the first and the second calculating devices can be eliminated.

10 [0015] The correction offset signals respectively added to the amplifiers are signals separated from the first and the second correction offset values. Because the second offset value calculated from the first calculating device is "0" and the first offset value calculated from the second calculating device is "0", the offset voltages calculated from the first calculating devices can be eliminated without using the unnecessary second correction offset value and the offset voltages calculated from the second calculating devices can also be eliminated without using the unnecessary first correction offset value.

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BRIEF DESCRIPTION OF THE DRAWINGS

[0016] While the specification concludes with claims particularly pointing out and

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distinctly claiming the subject matter which is regarded as the invention, the objects and features of the invention and further objects, features and advantages thereof will be better understood from the following description taken in connection with the accompanying drawings in which:

5 [0017] Fig. 1 is a block diagram of an optical disc device according to one preferred embodiment of the invention;

[0018] Fig. 2 is a first exemplary circuit diagram for the servo signal generator according to the invention;

[0019] Fig. 3 is a portion of the circuit diagram in Fig. 2;

10 [0020] Fig. 4 is a portion of the circuit diagram in Fig. 2;

[0021] Fig. 5 is a portion of the circuit diagram in Fig. 2;

[0022] Fig. 6 is a portion of the circuit diagram in Fig. 2;

[0023] Fig. 7 is a second exemplary circuit diagram for the servo signal generator according to the invention;

15 [0024] Figs. 8A and 8B are waveforms for illustrating the recording signal and the power of the light beam for the erasable optical disc;

[0025] Fig. 9 is an exemplary circuit diagram of a conventional servo signal generator;
and

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[0026] Fig. 10 is an exemplary circuit diagram of a conventional servo signal generator

DESCRIPTION OF THE PREFERRED EMBODIMENT

[0027] Fig. 1 is a block diagram of an optical disc device according to one preferred embodiment of the invention. A recordable disc 40, such as a CD-R or CD-RW type disc, is rotationally driven with a preset rotational speed by a spindle motor (not shown). An optical pick-up 42 is driven to move along the radial direction of the recordable disc 40 by a sled motor (not shown). The optical pick-up 42, in general, comprises an objective lens, an actuator, a 1/4 wave plate, a collimator lens, a beam splitter, a light source (such as a laser diode), a front monitor and photo detectors, etc.

[0028] A laser diode (LD) controller 44 is used for controlling the laser diode in the optical pick-up 42 to emit laser beams with a read power P_b when reproducing data, or with an erase power P_e , a write power P_w and a read power P_b according to recording pulses when data is recorded. Additionally, the LD controller 44 is capable of controlling a laser driver to output laser beams with an optimal power according to the intensity of the laser beams detected by the front monitor in the optical pick-up 42.

[0029] A radio frequency (RF) amplifier 46 is used for amplifying a reproduced signal reproduced from the optical disc 40 by means of the photo detectors in the optical pick-up 42. The reproduced signal amplified by the RF amplifier 46 is transmitted to a signal-reproducing process circuit 48 and a servo signal generator 50.

[0030] The signal-reproducing process circuit 48 executes processes such as a

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decoding process in a cross interleaved Read-Solomon code (CIRC), an eight-to-fourteen modulation (EFM), and a synchronizing process, etc. Furthermore, the signal-reproducing process circuit 48 executes a decoding process for an error correct code (ECC) of the CD-ROM and a detecting process of the header. The reproduced
5 data is then provided to a post-stage circuit (not shown) of the optical disc device.

[0031] The recording pulses from a terminal 53 are inputted to the LD controller 44 and a sampling pulse generator 52. The sampling pulse generator 52 generates a sampling/hold signal according to the recording pulses, which are supplied to the servo signal generator 50. A correction offset voltage for an addition signal and a correction
10 offset voltage for a subtraction signal is pre-held in an offset provider 54 and is supplied to the servo signal generator 50.

[0032] The servo signal generator 50 is used for sampling-hold detected signals of the photo detector according to the sampling/hold signal from the sampling pulse generator 52. The servo signal generator 50 adds the correction offset voltages of the addition
15 signal and the subtraction signal from the offset provider 54 to generate an addition signal, which serves as a reflected light monitoring signal, and a subtraction signal, which serves as a servo signal. The addition signal and the subtraction signal serving as a servo signal are then provided to the servo circuit 56. The servo circuit 56 drives the actuator in the optical pick-up 42 to perform a tracking servo control and a focusing
20 servo control.

[0033] Fig. 2 is a first exemplary circuit diagram for the servo signal generator according to the present invention. As shown in Fig. 2, spots of the light beams are

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illuminated on the optical disc 40 and the light beams reflected by the optical disc 40 are detected by four divided photo detectors 60A, 60B, 60C and 60D. The respective detected signals by the photo detectors 60A, 60B, 60C and 60D are transmitted to the corresponding sampling/hold circuits SA, SB, SC and SD.

5 [0034] Each of the sampling/hold circuits SA, SB, SC and SD consists of a switch and a condenser. Based on the sampling/hold signal, for example, a high-level timing, the switch of the sampling/hold circuit is on to sample the detected signals. The sampled detected signals are then held by the condenser of the sampling/hold circuit. The signals sampled and held by the sampling/hold circuits SA, SB, SC and SD are
10 respectively provided to the corresponding amplifiers 66A, 66B, 66C and 66D.

[0035] According to a gain-switching signal from a terminal 64, the gains of the amplifiers 66A, 66B, 66C and 66D switch to different gains, which correspond to the foregoing detected signals when data are recorded and reproduced. Switching to different gains also causes the levels of the detected signals to have the same level when
15 data are being recorded and reproduced. In addition, the amplifiers 66A, 66B add the corresponding detected signals with the correction offset voltage of the addition signal Vosadd from the terminal 68 and the correction offset voltage of the subtraction signal Vossb from the terminal 70. Moreover, the amplifiers 66C, 66D add the corresponding detected signals with the correction offset voltage of the addition signal
20 Vosadd from the terminal 68 and subtract the correction offset voltage of the subtraction signal Vossb from the terminal 70.

[0036] Output signals A, B, C and D outputted from the amplifiers 66A, 66B, 66C

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and 66D are inputted to the calculating devices 72, 76. The calculating device 72 adds the detected signals A, B, C and D to generate an addition signal (A+B+C+D), which serves as the reflected light monitoring signal and is outputted from the terminal 74. Also, the calculating device 76 adds the detected signals A, B and subtracts the signals C, D to generate a subtraction signal (A+B)-(C+D), which serves as the tracking servo signal and is outputted from the terminal 78.

[0037] Fig. 3 is a portion of the circuit diagram in Fig. 2. With Fig. 3, the correction offset voltage Vosadd in the addition signal (A+B+C+D) is described in detail as follows.

[0038] Assuming that the offset voltages of the detected signals A, B, C and D respectively outputted from the sampling/hold circuits SA, SB, SC, SD are Va, Vb, Vc, and Vd, the input offset voltages of the amplifiers 66A, 66B, 66C, 66D are Vga, Vgb, Vgc, and Vgd respectively, the gains of the amplifiers 66A, 66B, 66C, 66D are G, and the calculating devices 72, 76 have no offset voltages. Under this condition, the total offset voltage of the addition signal Vadd is represented by the equation (3) below.

$$[0039] \quad V_{add} = G(V_a + V_b + V_c + V_d + V_{ga} + V_{gb} + V_{gc} + V_{gd}) \quad (3)$$

Therefore, if $4Gv_{osadd} = -V_{add}$, the total offset voltage of the addition signal can be compensated.

$$\begin{aligned}
 & \boxed{V_{osadd} = -V_{add} / (4 \cdot G)} \\
 & = -[(V_a + V_b + V_c + V_d + V_{ga} + V_{gb} + V_{gc} + V_{gd}) / 4] \quad (4)
 \end{aligned}$$

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Accordingly, the correction offset voltage is not affected by the switch of the gain G because the gain G is not in the correction offset voltage of the addition signal V_{osadd} .

[0040] Fig. 4 is a portion of the circuit diagram in Fig. 2. With Fig. 4, the influence of the correction offset voltage V_{ossub} in the subtraction signal $(A+B)-(C+D)$ is described in detail as follows. A total offset voltage of the subtraction signal V_{sub} is represented in following equation (5).

$$\begin{aligned}
 [0041] \quad V_{sub} &= G [(V_a + V_b + V_{ga} + V_{gb}) - (V_c + V_d + V_{gc} + V_{gd})] \\
 &\quad + G (2V_{osadd} - 2V_{osadd}) \\
 &= G [(V_a + V_b + V_{ga} + V_{gb}) - (V_c + V_d + V_{gc} + V_{gd})] \quad (5)
 \end{aligned}$$

10 Namely, equation (5) concludes that the correction offset voltage of the addition signal V_{osadd} has no influence on the total offset voltage of the subtraction signal.

[0042] Next, Fig. 5 is a portion of the circuit diagram in Fig. 2. With Fig. 5, the correction offset voltage V_{ossub} in the subtraction signal $(A+B)-(C+D)$ is described in detail as follows. The total offset voltage of the subtraction signal V_{sub} is represented by the foregoing equation (5). Therefore, if $4Gv_{ossub} = -V_{sub}$, the total offset voltage of the subtraction signal V_{sub} can be compensated.

$$V_{ossub} = -V_{sub} / (4 \cdot G)$$

$$= - \{ [(V_a + V_b + V_{ga} + V_{gb}) - (V_c + V_d + V_{gc} + V_{gd})] / 4 \} \quad (6)$$

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Accordingly, the correction offset voltage is not affected by the switch of the gain G because the gain G is not in the correction offset voltage of the subtraction signal V_{osub}.

[0043] Fig. 6 is a portion of the circuit diagram in Fig. 2. With Fig. 6, the influence of the correction offset voltage V_{osub} in the addition signal (A+B+C+D) is described in detail as follows. A total offset voltage of the addition signal V_{add} is represented in following equation (7).

[0044]

$$\begin{aligned} V_{add} &= G (V_a + V_b + V_c + V_d + V_{ga} + V_{gb} + V_{gc} + V_{gd}) + G (2V_{osub} - 2V_{osub}) \\ &= G (V_a + V_b + V_c + V_d + V_{ga} + V_{gb} + V_{gc} + V_{gd}) \end{aligned} \quad (7)$$

Namely, it is the same result as in equation (3). Equation (3) concludes that the correction offset voltage of the subtraction signal V_{osub} has no influence on the total offset voltage of the addition signal.

[0045] Fig. 7 is a second exemplary circuit diagram for the servo signal generator according to the invention. The same labels and reference numbers represent the same elements in Fig. 2. As shown in Fig. 7, spots of the light beams are illuminated on the optical disc 40 and the light beams reflected by the optical disc 40 are detected by four divided photo detectors 60A, 60B, 60C and 60D. The respective detected signals by the photo detectors 60A, 60B, 60C and 60D are transmitted to the corresponding sampling/hold circuits SA, SB, SC and SD.

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[0046] Each of the sampling/hold circuits SA, SB, SC and SD consists of a switch and a condenser. Based on the sampling/hold signal, for example, a high-level timing, the switch of the sampling/hold circuit is on to sample the detected signals. The sampled detected signals are then held by the condenser of the sampling/hold circuit. The signals sampled and held by the sampling/hold circuits SA, SB, SC and SD are then respectively provided to the corresponding amplifiers 80A, 80B, 80C and 80D.

[0047] According to a gain-switching signal from a terminal 64, the gains of the amplifiers 80A, 80B, 80C and 80D switch to different gains, which correspond to the foregoing detected signals when data are recorded and reproduced. Switching to different gains causes the levels of the detected signals to have the same level when data are recorded and reproduced. In addition, the amplifiers 80A add the corresponding detected signals with the correction offset voltage of the addition signal Vosadd from the terminal 68 and a correction offset voltage of the tracking error signal Voste from the terminal 84. Also, the amplifiers 80C add the corresponding detected signals with the correction offset voltage of the addition signal Vosadd and subtracts from a correction offset voltage of a focusing error signal Vosfe from the terminal 82.

[0048] In addition, the amplifiers 80D adds the corresponding detected signals with the correction offset voltage of the focusing error signal Vosfe, and subtracts from the correction offset voltage of a tracking error signal Voste. The correction offset is not added to the amplifier 80B. The output signals A, B, C and D from the amplifiers 80A, 80B, 80C and 80D are respectively inputted to calculating devices 86, 88 and 90.

[0049] The calculating device 86 adds the detected signals A, B, C and D to generate

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an addition signal (A+B+C+D), which serves as the reflected light monitoring signal and is outputted from the terminal 87. In addition, the calculating device 88 adds the detected signals A, D and subtracts the signals B, C to generate a subtraction signal (A+B)-(C+D), which serves as the focusing servo signal and is outputted from the terminal 89. Also, the calculating device 90 adds the detected signals A, B and subtracts the signals C, D to generate a subtraction signal (A+B)-(C+D), which serves as the tracking servo signal and is outputted from the terminal 91.

[0050] Assuming that the total offset voltage of the addition signal is V_{add} , the correction offset voltage of the addition signal is V_{osadd} , the total offset voltage of the focusing servo signal is V_{fe} , the correction offset voltage of the focusing servo signal is V_{osfe} , the total offset voltage of the tracking servo signal is V_{te} , the correction offset voltage of the tracking servo signal is V_{oste} , the offset voltages of the detected signals A, B, C and D respectively outputted from the sampling/hold circuits SA, SB, SC, SD are V_a , V_b , V_c , and V_d , the input offset voltages of the amplifiers 80A, 80B, 80C, 80D are V_{ga} , V_{gb} , V_{gc} , and V_{gd} respectively, the gains of the amplifiers 80A, 80B, 80C, 80D are G, and the calculating devices 86, 88, 90 have no offset voltages. In the condition, the total offset voltage of the addition signal V_{add} is represented by the equation (8) below.

$$[0051] \quad V_{add} = -G [(V_{osadd} + V_{oste}) + (V_{osadd} - V_{osfe}) + (V_{osfe} - V_{oste})]$$

$$20 \quad \quad \quad = -2GV_{osadd} \quad (8)$$

Therefore, if $2GV_{osadd} = -V_{add}$, the total offset voltage of the addition signal can be compensated. In addition, because of $V_{add} = G (V_a + V_b + V_c + V_d + V_{ga} + V_{gb} +$

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Vgc + Vgd), Vosadd can be represented by following equation:

$$Vosadd = - Vadd / (2G)$$

$$= - (Va + Vb + Vc + Vd + Vga + Vgb + Vgc + Vgd) / 2.$$

Accordingly, the correction offset voltage of the addition signal Vosadd is not affected
 5 by the switch of the gain G because the gain G is not in the correction offset voltage of
 the addition signal Vosadd.

[0052] In addition, the total focusing offset voltage of the focusing servo signal is
 represented by following equation (9):

$$\begin{aligned} Vfe &= - G [(Vosadd + Voste) + (Vosfe - Voste) - (Vosadd - Vosfe)] \\ 10 \qquad \qquad \qquad &= -2GVosfe \qquad \qquad \qquad (9) \end{aligned}$$

Therefore, if $2GVosfe = -Vfe$, the total offset voltage of the focusing servo signal can be
 compensated. In addition, because of $Vfe = G (Va - Vb - Vc + Vd + Vga - Vgb -$
 $Vgc + Vgd)$, Vosfe can be represented by following equation:

$$\begin{aligned} Vosfe &= - Vfe / (2G) \\ 15 \qquad \qquad \qquad &= - [(Va - Vb - Vc + Vd + Vga - Vgb - Vgc + Vgd)] / 2. \end{aligned}$$

Accordingly, the correction offset voltage is not affected by the switch of the gain G
 because the gain G is not in the correction offset voltage of the focusing servo signal
 Vosfe.

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[0053] In addition, the total offset voltage of the tracking servo signal is represented by following equation (10):

$$\begin{aligned} V_{te} &= -G [(V_{osadd} + V_{oste}) - (V_{osadd} - V_{osfe}) - (V_{osfe} - V_{oste})] \\ &= -2GV_{oste} \end{aligned} \quad (10)$$

- 5 Therefore, if $2GV_{oste} = -V_{te}$, the total offset voltage of the tracking servo signal can be compensated. In addition, because of $V_{te} = G (V_a + V_b - V_c - V_d + V_{ga} + V_{gb} - V_{gc} - V_{gd})$, V_{oste} can be represented by following equation:

$$\begin{aligned} V_{oste} &= -V_{te} / (2G) \\ &= -[(V_a + V_b - V_c - V_d + V_{ga} + V_{gb} - V_{gc} - V_{gd})] / 2. \end{aligned}$$

- 10 Accordingly, the correction offset voltage is not affected by the switch of the gain G because the gain G is not in the correction offset voltage of the tracking servo signal V_{oste} .

- [0054] In addition, referring to claim 1, the calculating device 72 corresponds to the first calculating device, while the calculating device 76 corresponds to the second calculating device. The correction offset voltage for the addition signal V_{osadd} corresponds to the first correction offset value, while the correction offset voltage for the subtraction signal V_{ossub} corresponds to the second correction offset value.

[0055] According to the foregoing descriptions, the invention provides an optical disc device for changing the intensities of the light beams illuminated on an optical disc.

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The optical disc device comprises the following elements. A plurality of photo detectors is used for detecting the reflected light beams of the light beams illuminated on an optical disc. A plurality of amplifiers is used for changing gains to respectively amplify the output signals of the photo detectors when data are recorded and reproduced
5 on/from the optical disc. A calculating device is also used for calculating the output signals of the amplifiers to generate the servo signals. By means of adding the correction offset signals for correcting the offset voltages of the amplifiers and the photo detectors to the amplifiers, the correction offset signals are amplified and therefore it is not necessary to change the correction offset voltages even though the gain of the
10 amplifier is changed.

[0056] The calculating device further comprises a first calculating device and a second calculating device for respectively performing different operations on the output signals of the amplifiers. Because the correction offset signals respectively added to the amplifiers further comprise a first correction offset value that eliminates the offset
15 voltages calculated from the first calculating device and a second correction offset value that eliminates the offset voltages calculated from the second calculating device, the offset voltages calculated from the first and the second calculating devices can be eliminated.

[0057] The correction offset signals respectively added to a plurality of amplifiers are
20 signals separated from the first and the second correction offset values. Because the second offset value calculated from the first calculating device is "0" and the first offset value calculated from the second calculating device is "0", the offset voltages calculated from the first calculating devices can be eliminated without using the

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unnecessary second correction offset value and the offset voltages calculated from the second calculating devices can be eliminated without using the unnecessary first correction offset value.

5 [0058] While the present invention has been described with a preferred embodiment, this description is not intended to limit our invention. Various modifications of the embodiment will be apparent to those skilled in the art. It is therefore contemplated that the appended claims will cover any such modifications or embodiments as fall within the true scope of the invention.

19710 19711 19712 19713 19714 19715 19716 19717 19718 19719 19720 19721 19722 19723 19724 19725 19726 19727 19728 19729 19730 19731 19732 19733 19734 19735 19736 19737 19738 19739 19740 19741 19742 19743 19744 19745 19746 19747 19748 19749 19750 19751 19752 19753 19754 19755 19756 19757 19758 19759 19760 19761 19762 19763 19764 19765 19766 19767 19768 19769 19770 19771 19772 19773 19774 19775 19776 19777 19778 19779 19780 19781 19782 19783 19784 19785 19786 19787 19788 19789 19790 19791 19792 19793 19794 19795 19796 19797 19798 19799 19800 19801 19802 19803 19804 19805 19806 19807 19808 19809 19810 19811 19812 19813 19814 19815 19816 19817 19818 19819 19820 19821 19822 19823 19824 19825 19826 19827 19828 19829 19830 19831 19832 19833 19834 19835 19836 19837 19838 19839 19840 19841 19842 19843 19844 19845 19846 19847 19848 19849 19850 19851 19852 19853 19854 19855 19856 19857 19858 19859 19860 19861 19862 19863 19864 19865 19866 19867 19868 19869 19870 19871 19872 19873 19874 19875 19876 19877 19878 19879 19880 19881 19882 19883 19884 19885 19886 19887 19888 19889 19890 19891 19892 19893 19894 19895 19896 19897 19898 19899 19900 19901 19902 19903 19904 19905 19906 19907 19908 19909 19910 19911 19912 19913 19914 19915 19916 19917 19918 19919 19920 19921 19922 19923 19924 19925 19926 19927 19928 19929 19930 19931 19932 19933 19934 19935 19936 19937 19938 19939 19940 19941 19942 19943 19944 19945 19946 19947 19948 19949 19950 19951 19952 19953 19954 19955 19956 19957 19958 19959 19960 19961 19962 19963 19964 19965 19966 19967 19968 19969 19970 19971 19972 19973 19974 19975 19976 19977 19978 19979 19980 19981 19982 19983 19984 19985 19986 19987 19988 19989 19990 19991 19992 19993 19994 19995 19996 19997 19998 19999 20000